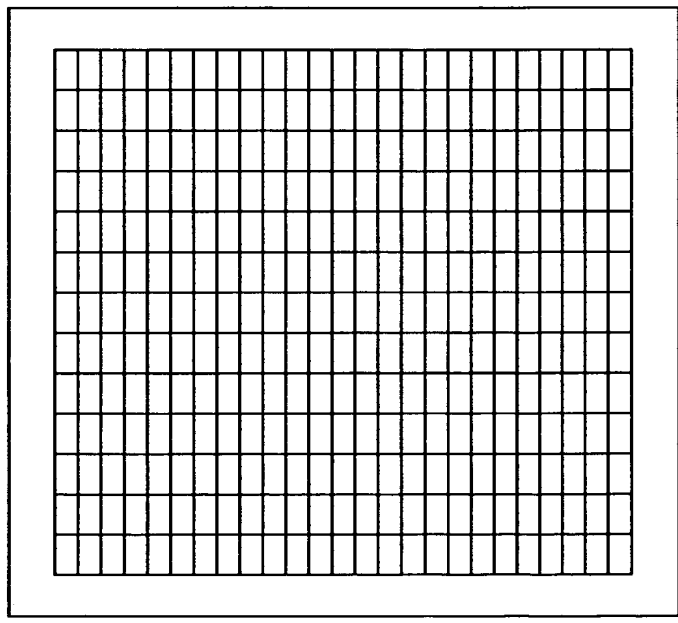


After Saw

FIG. 1B



6.6" Square Silicon Substrate

FIG. 1A

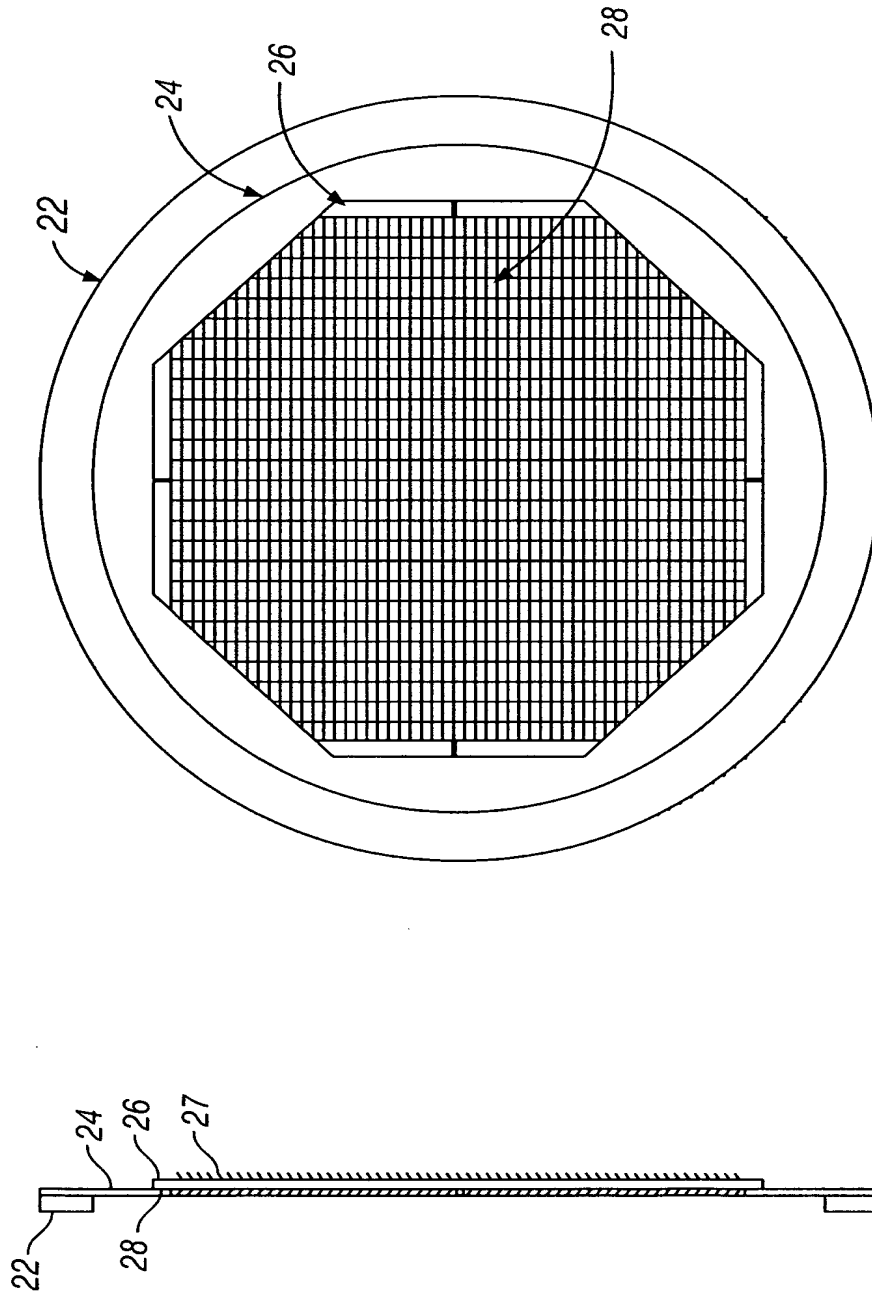


FIG. 2B

FIG. 2A

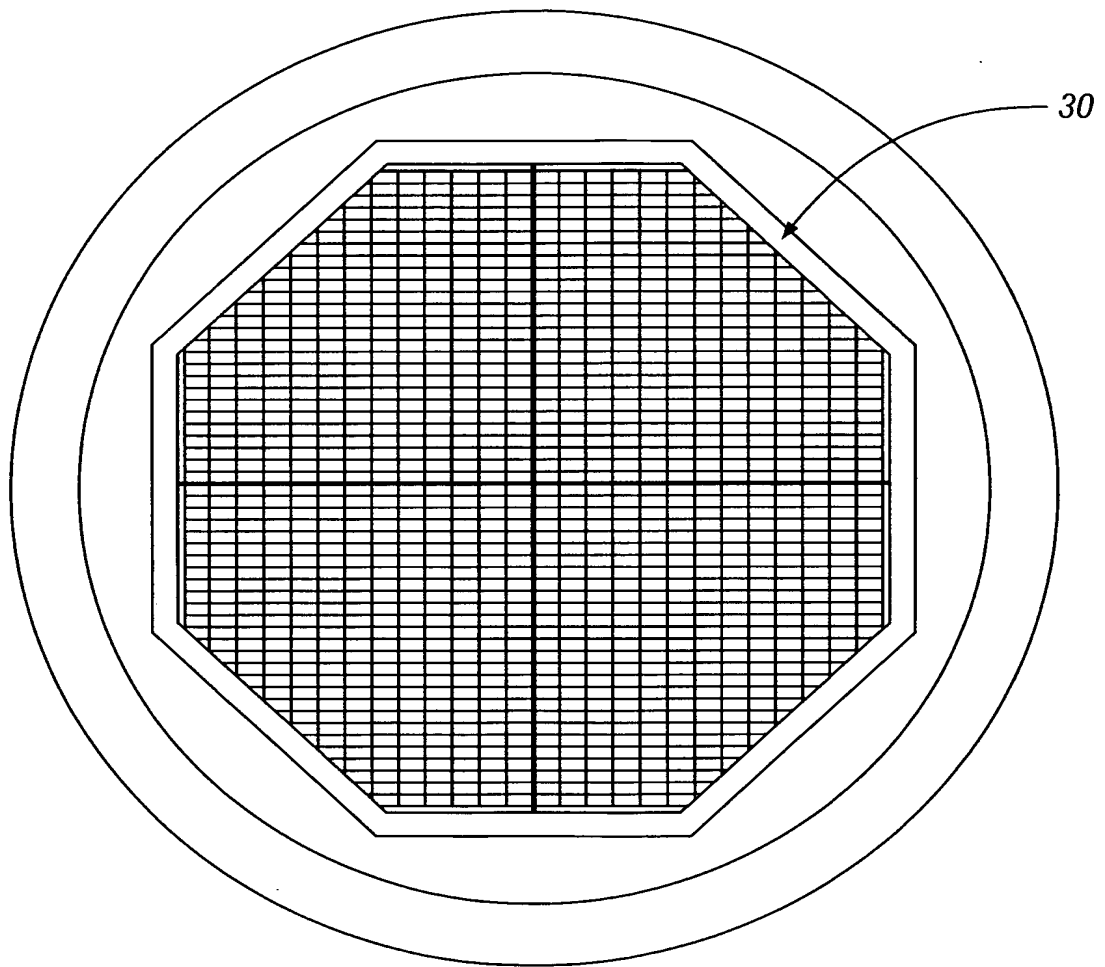
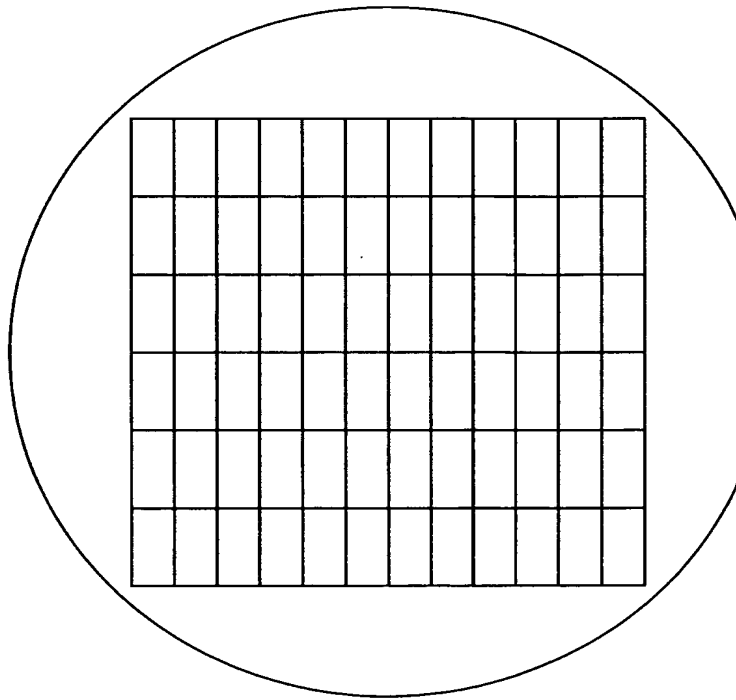
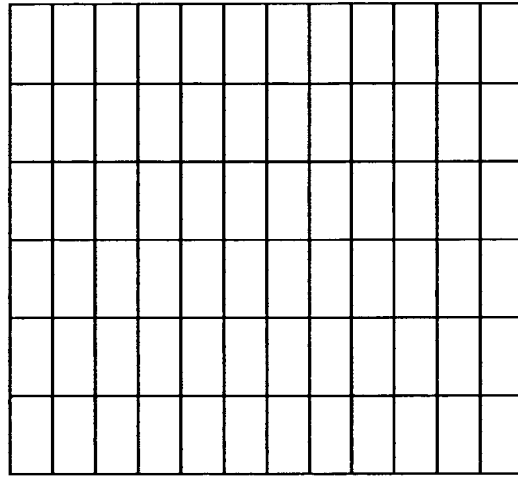


FIG. 3



4" Ceramic Wafer

FIG. 4A



Sawed Substrate

FIG. 4B

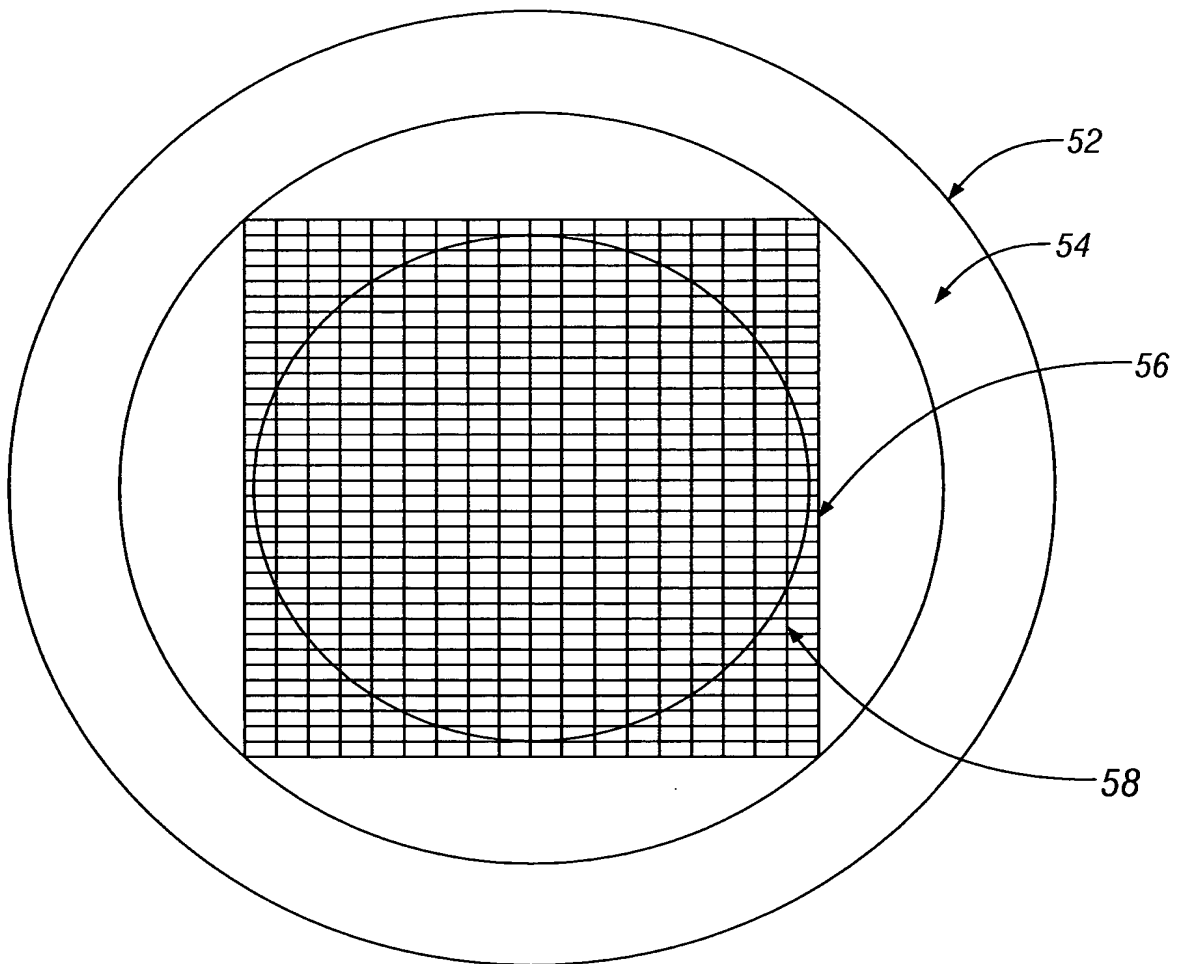


FIG. 5

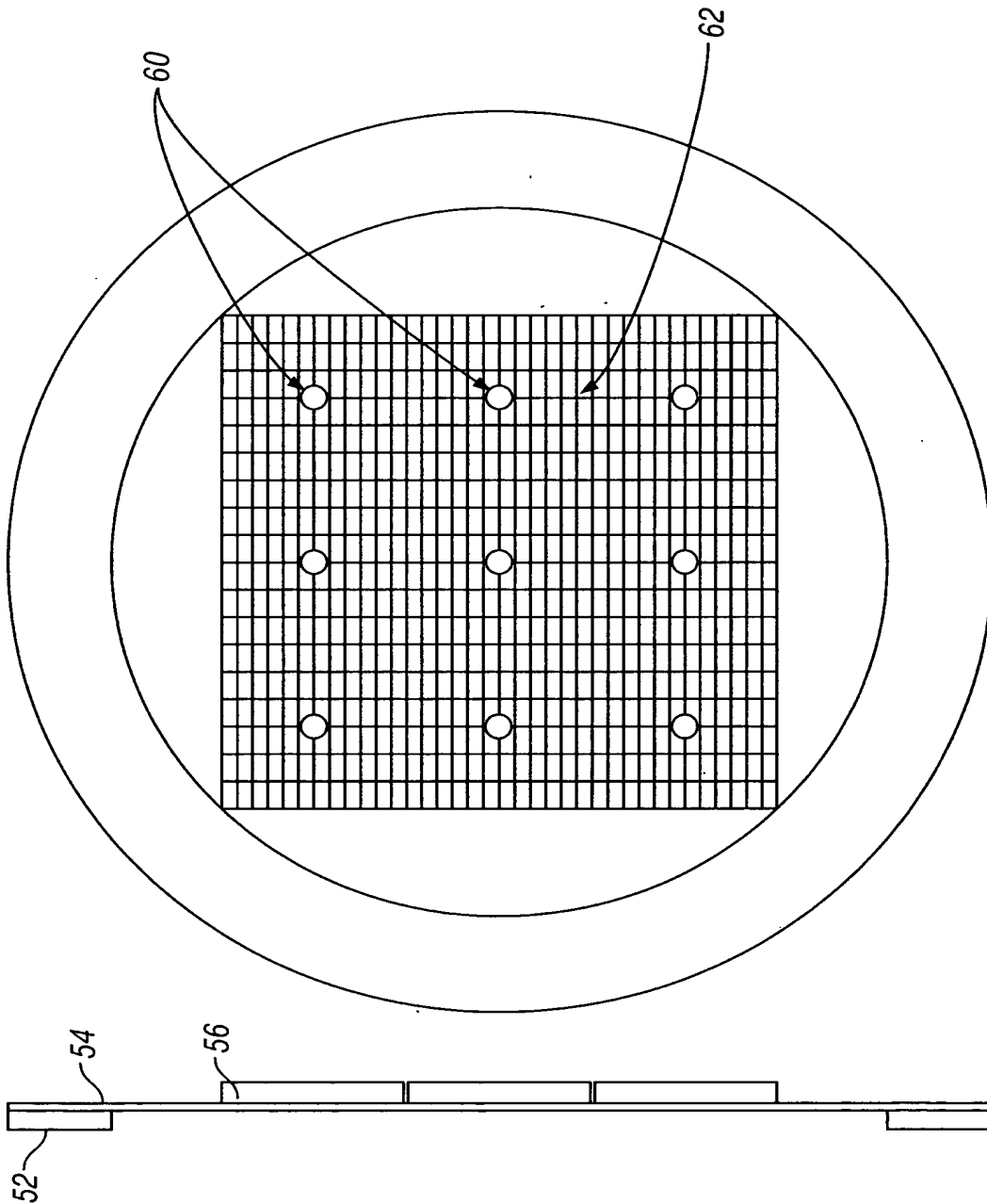


FIG. 6B

FIG. 6A

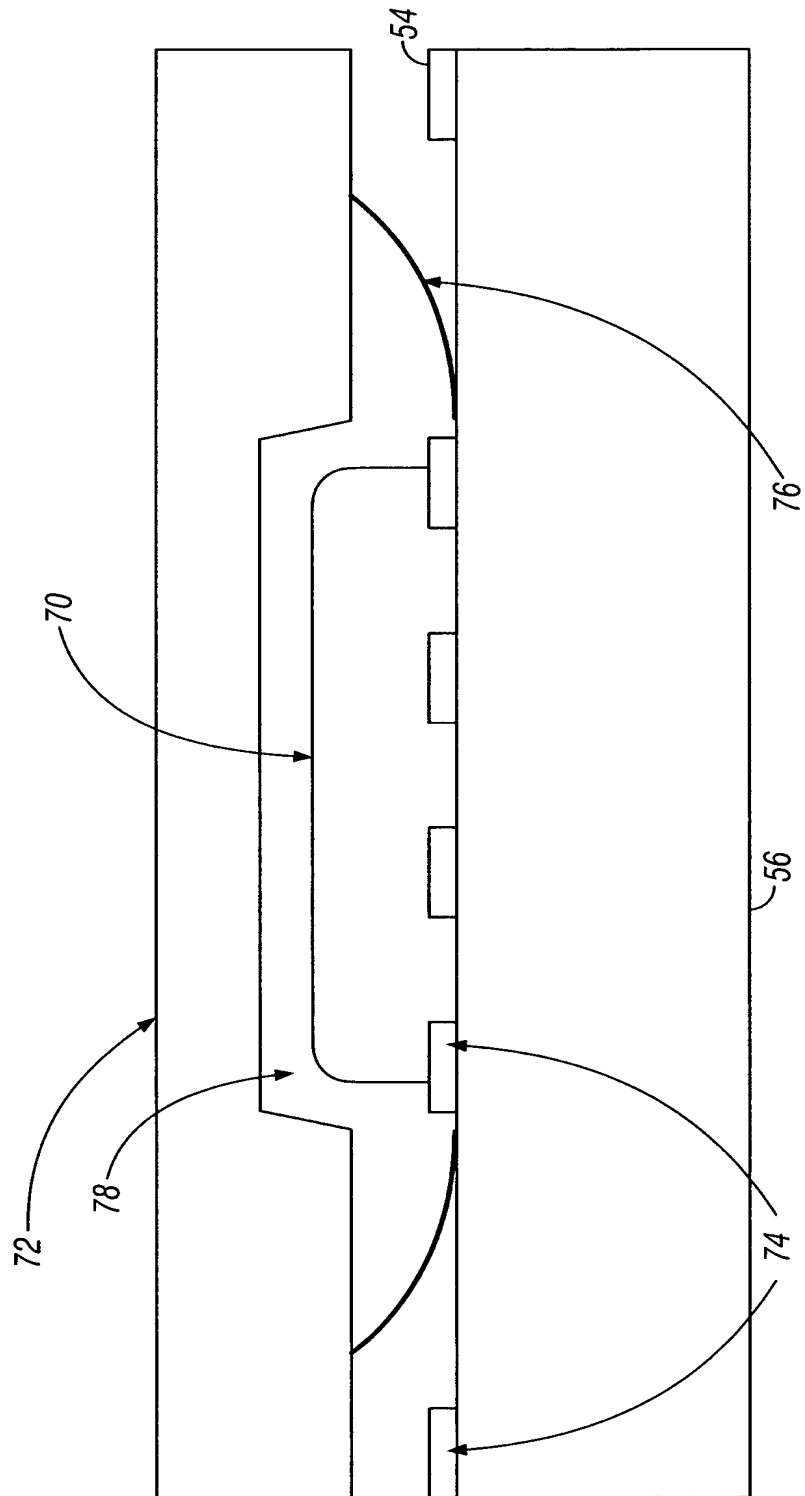


FIG. 7

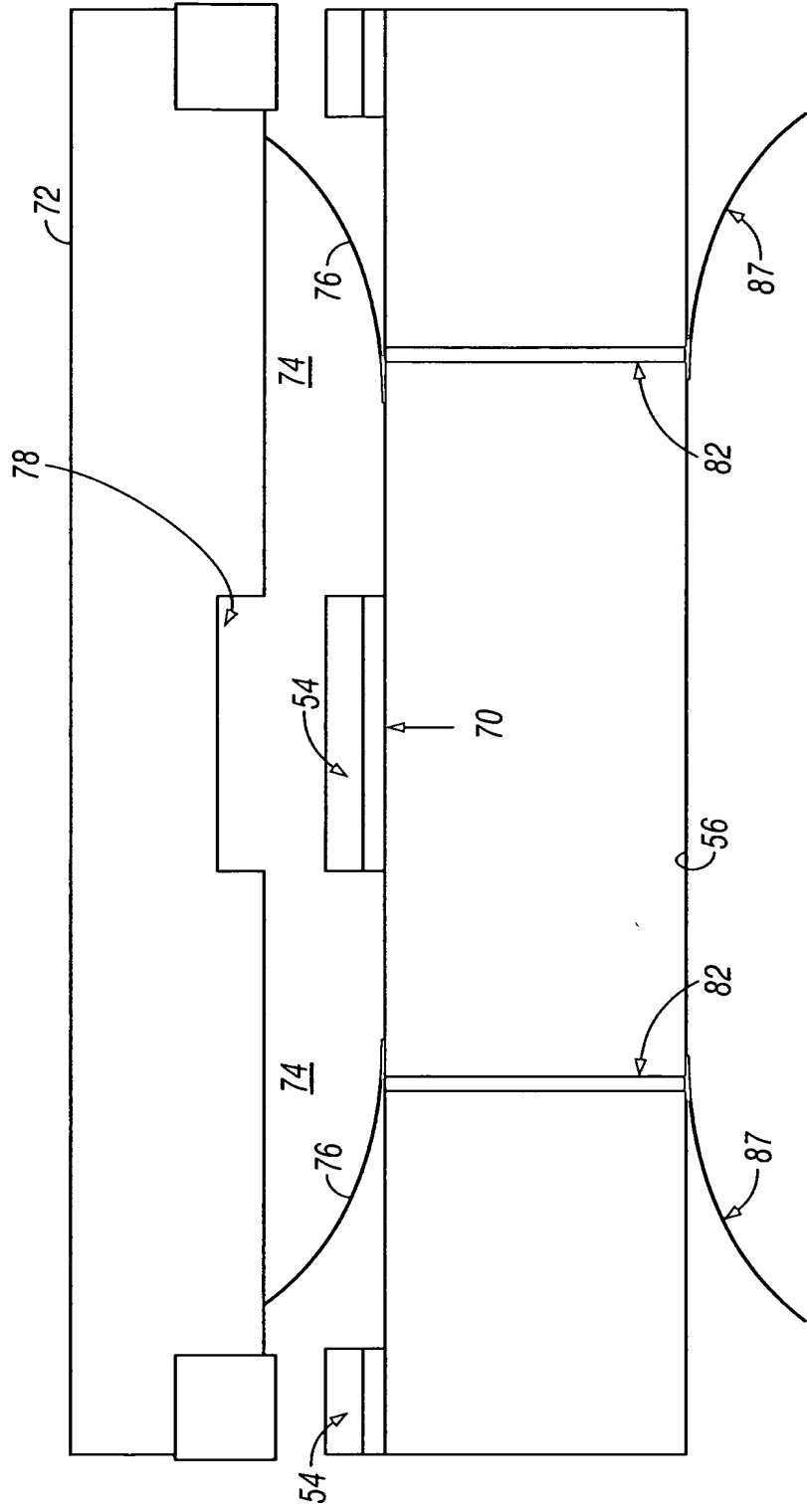
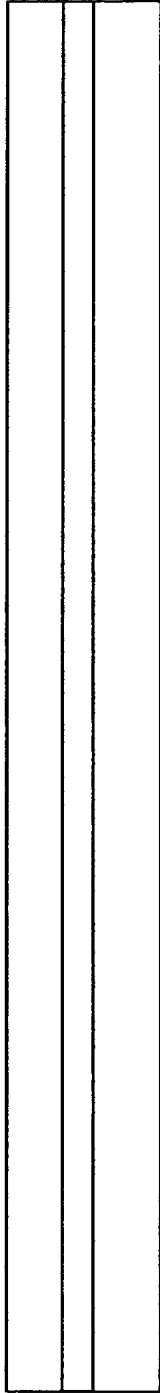


FIG. 8

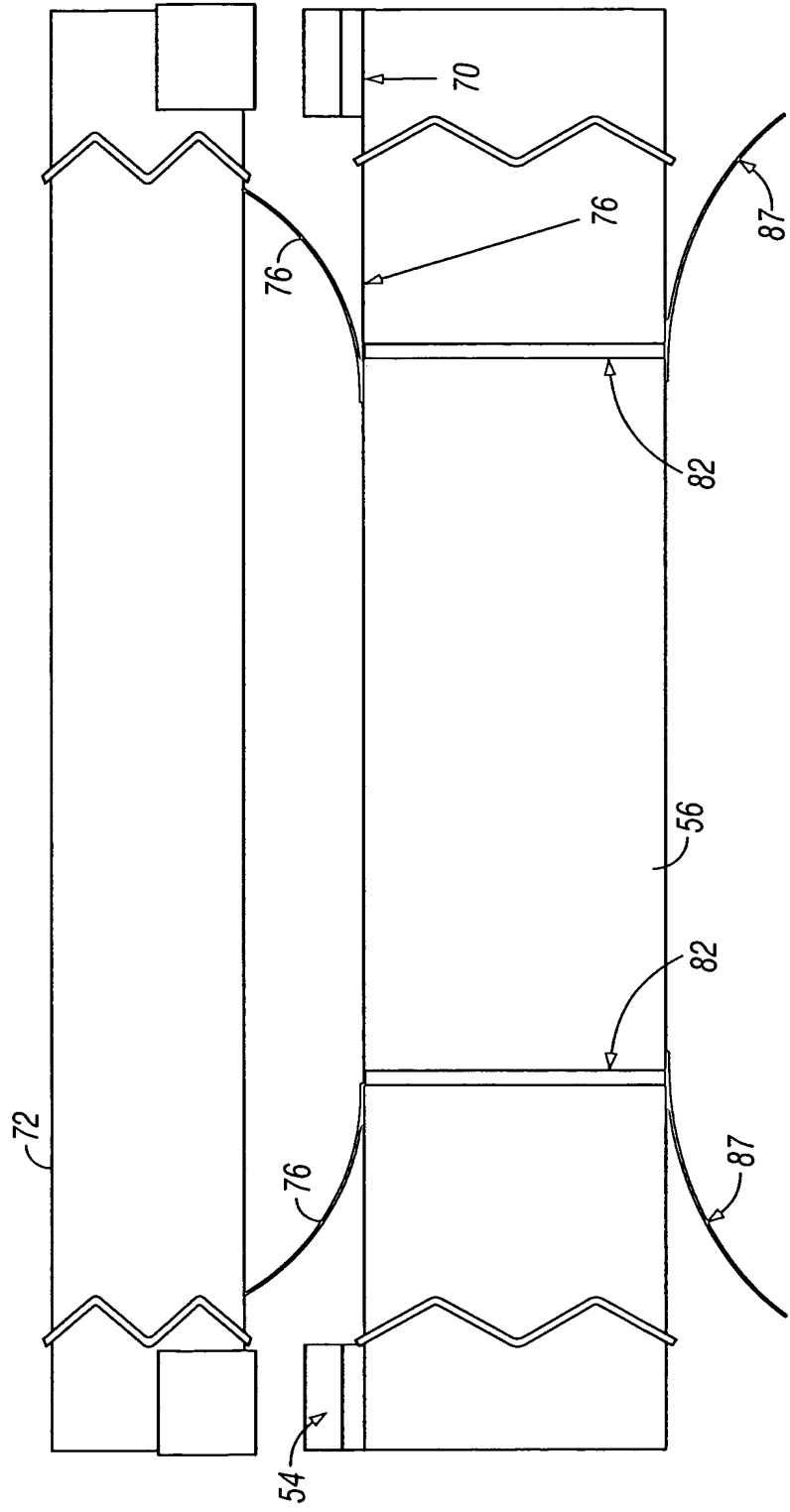
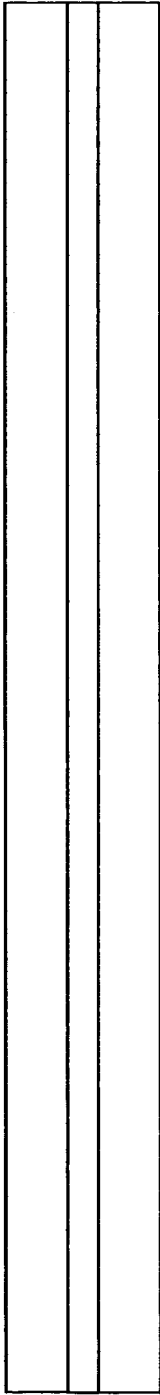


FIG. 9